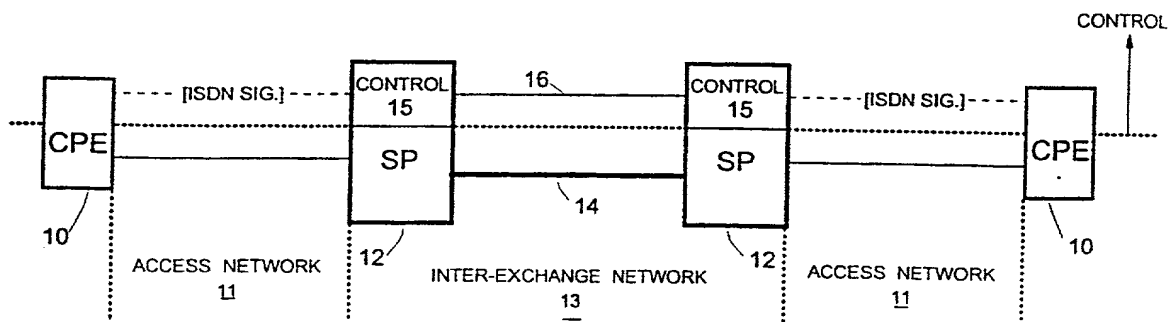
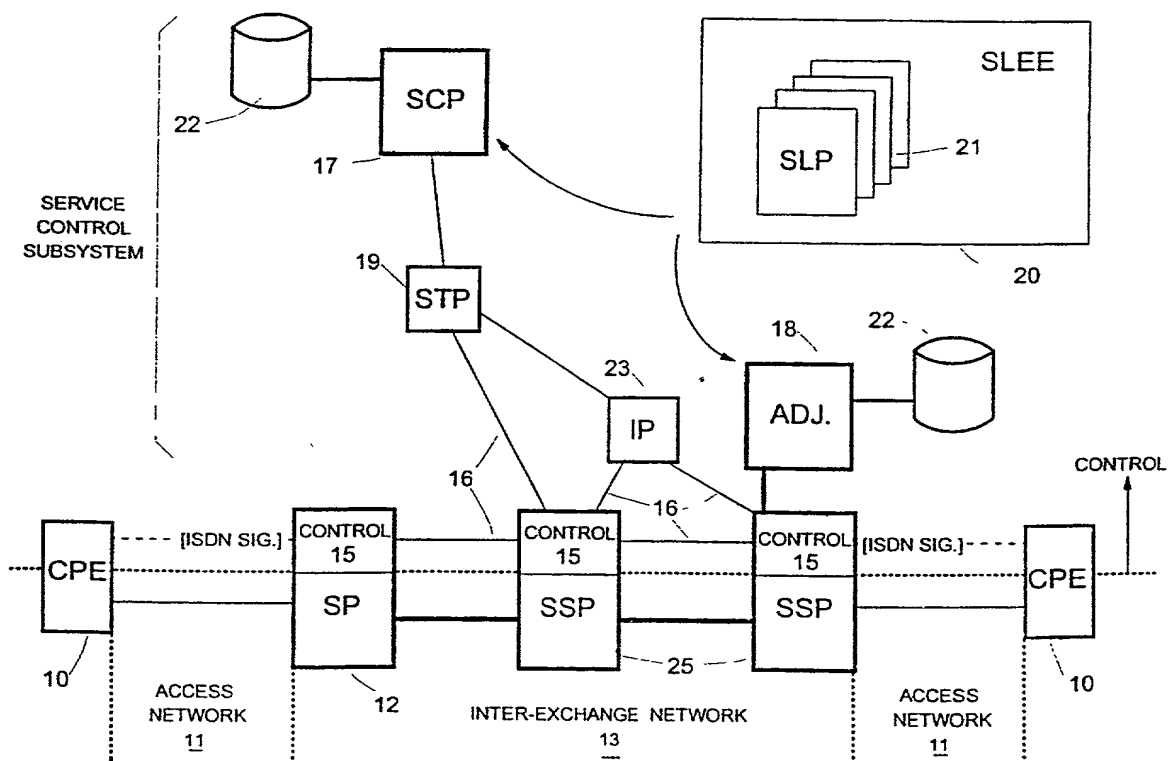


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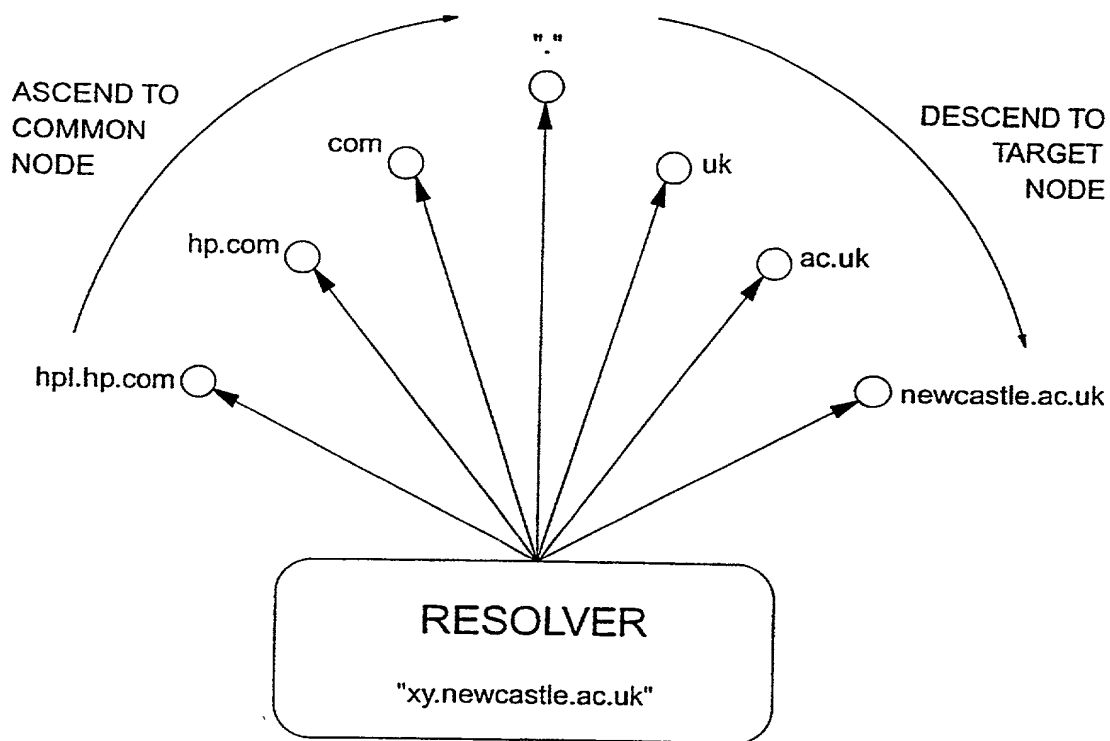


**FIG. 1**  
(PRIOR ART)



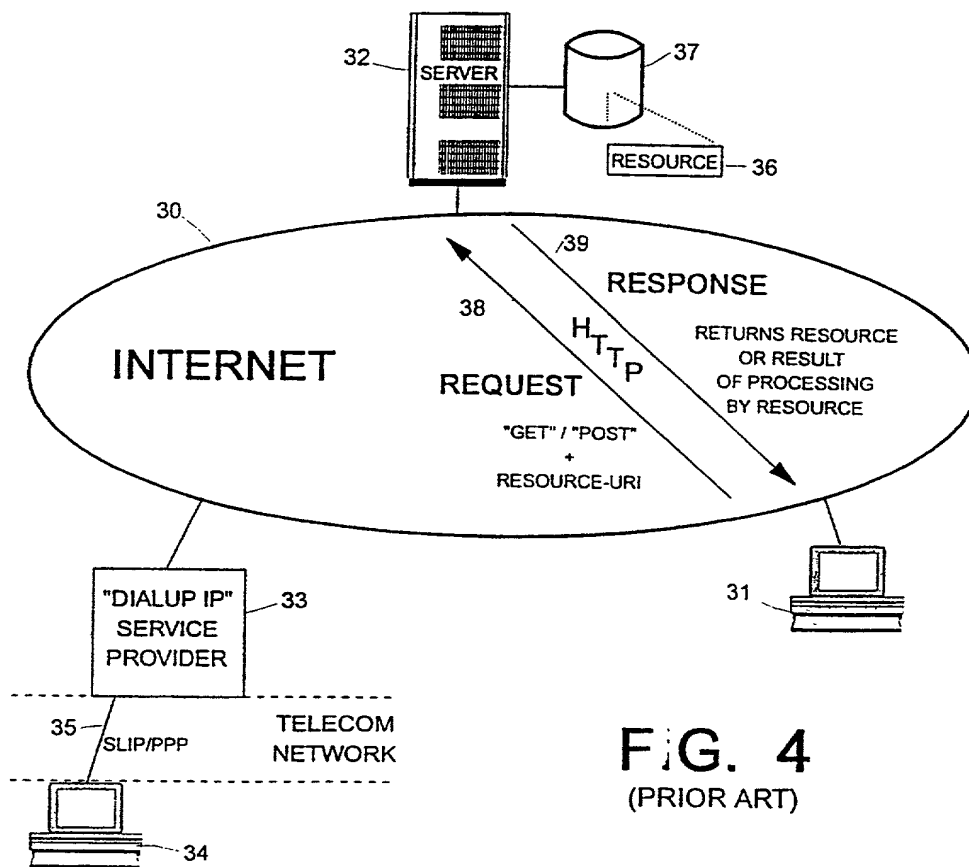
**FIG. 2**  
(PRIOR ART)

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**FIG. 3**  
(PRIOR ART)

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**FIG. 4**  
(PRIOR ART)

[http : //www.hp.com/Products.html](http://www.hp.com/Products.html)

SCHEME

HOST LOCATION

ABSOLUTE PATH

**FIG. 5**  
(PRIOR ART)



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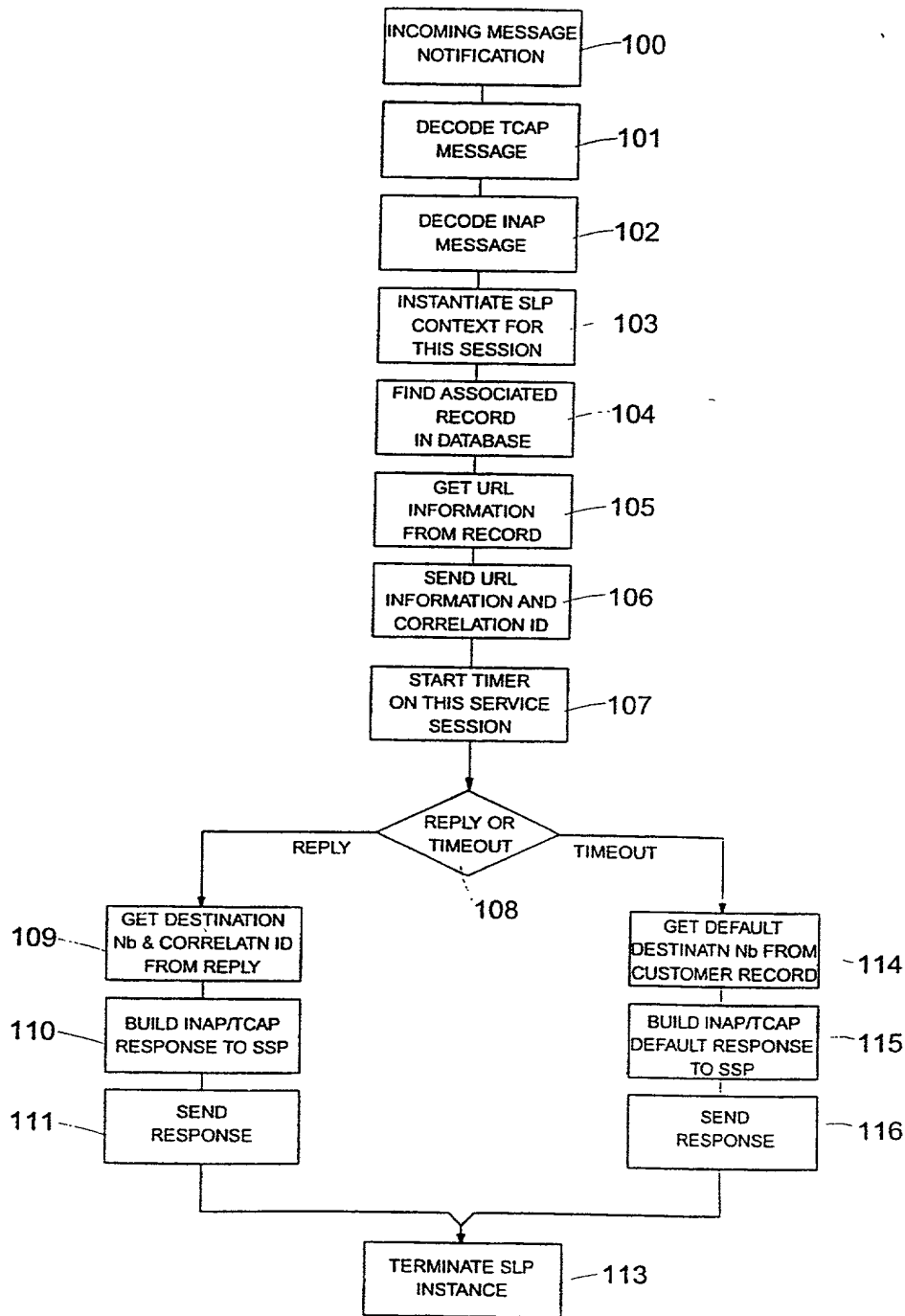


FIG. 7

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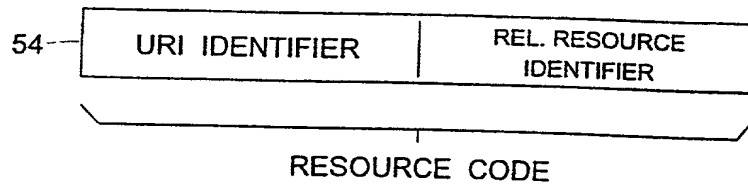


FIG. 8

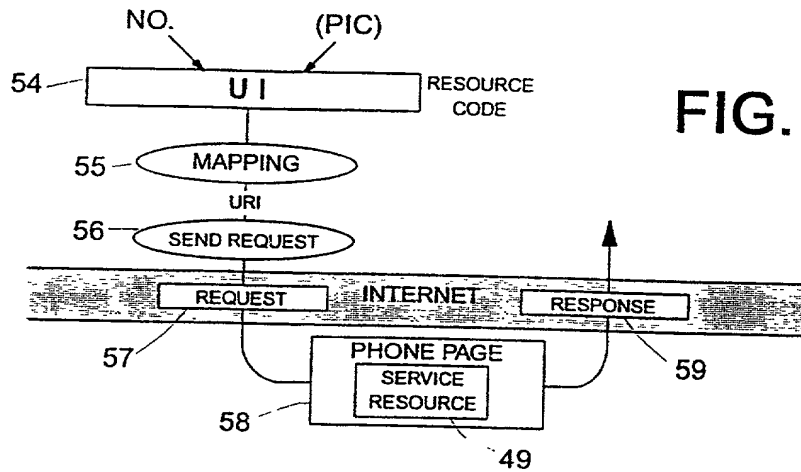


FIG. 9

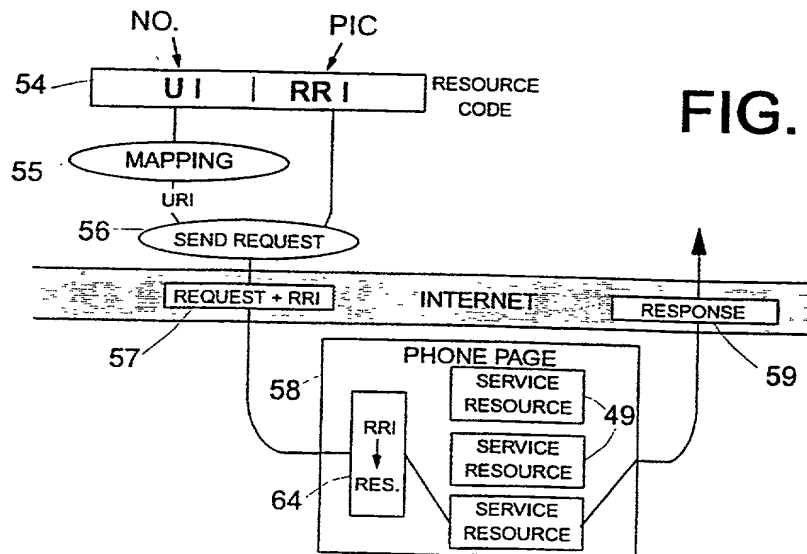


FIG. 10

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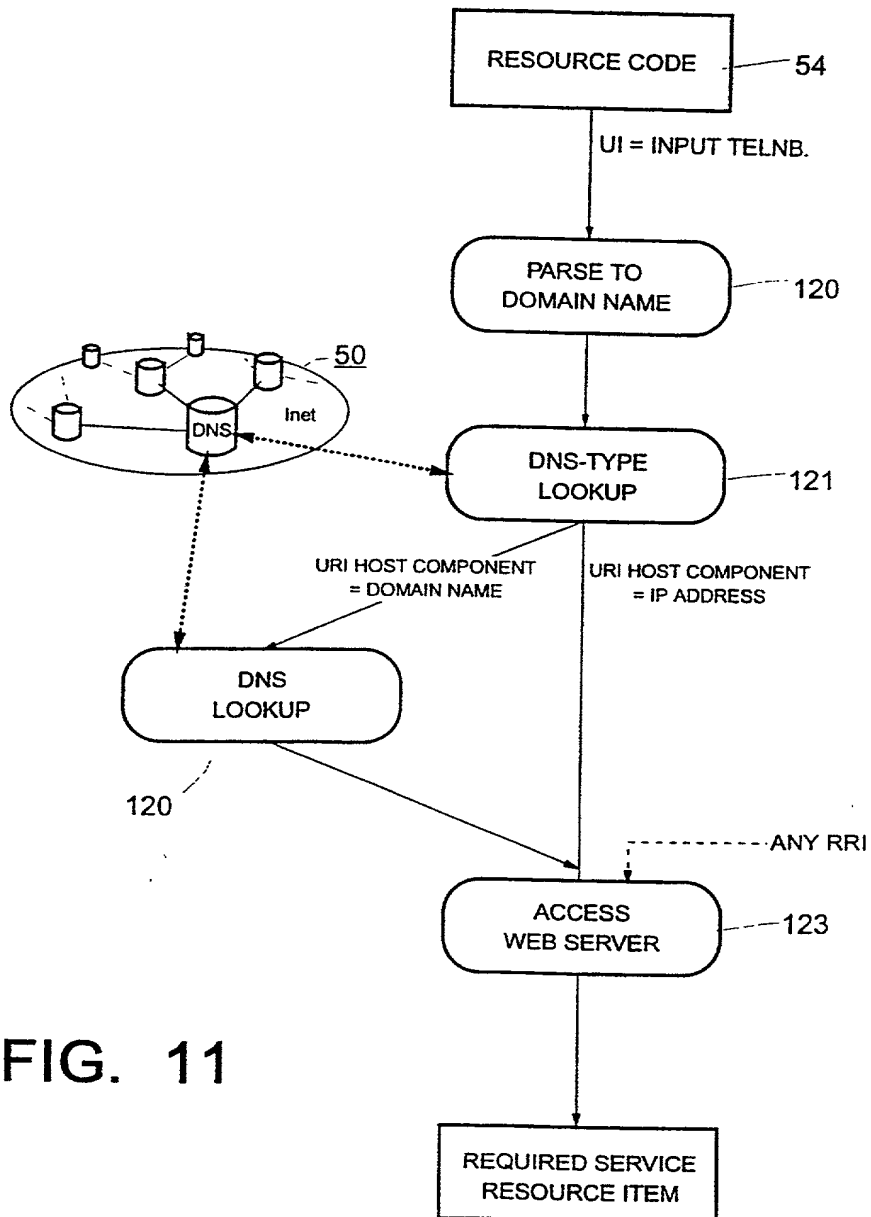


FIG. 11

[illegible]

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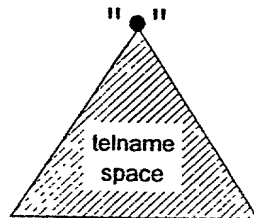


FIG. 12A

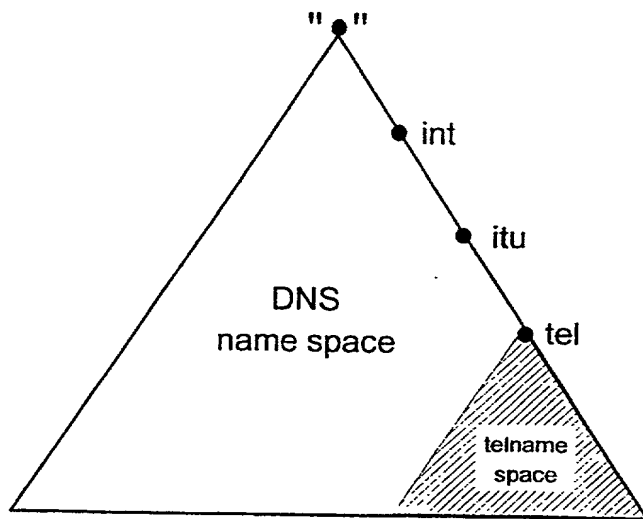


FIG. 12B

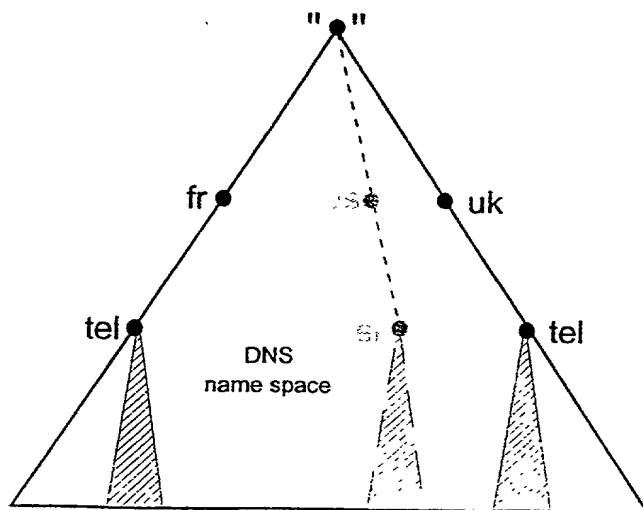


FIG. 12C



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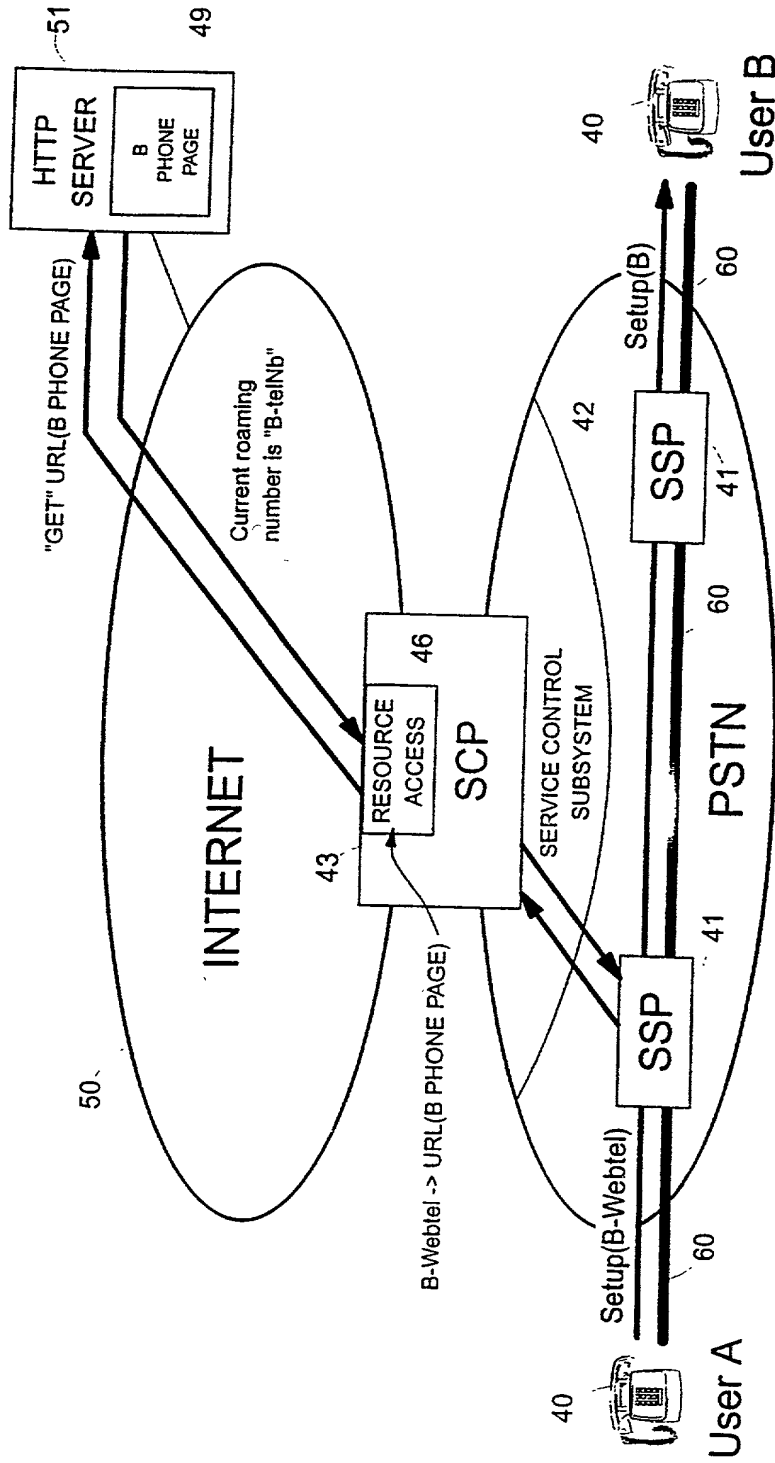


FIG. 13

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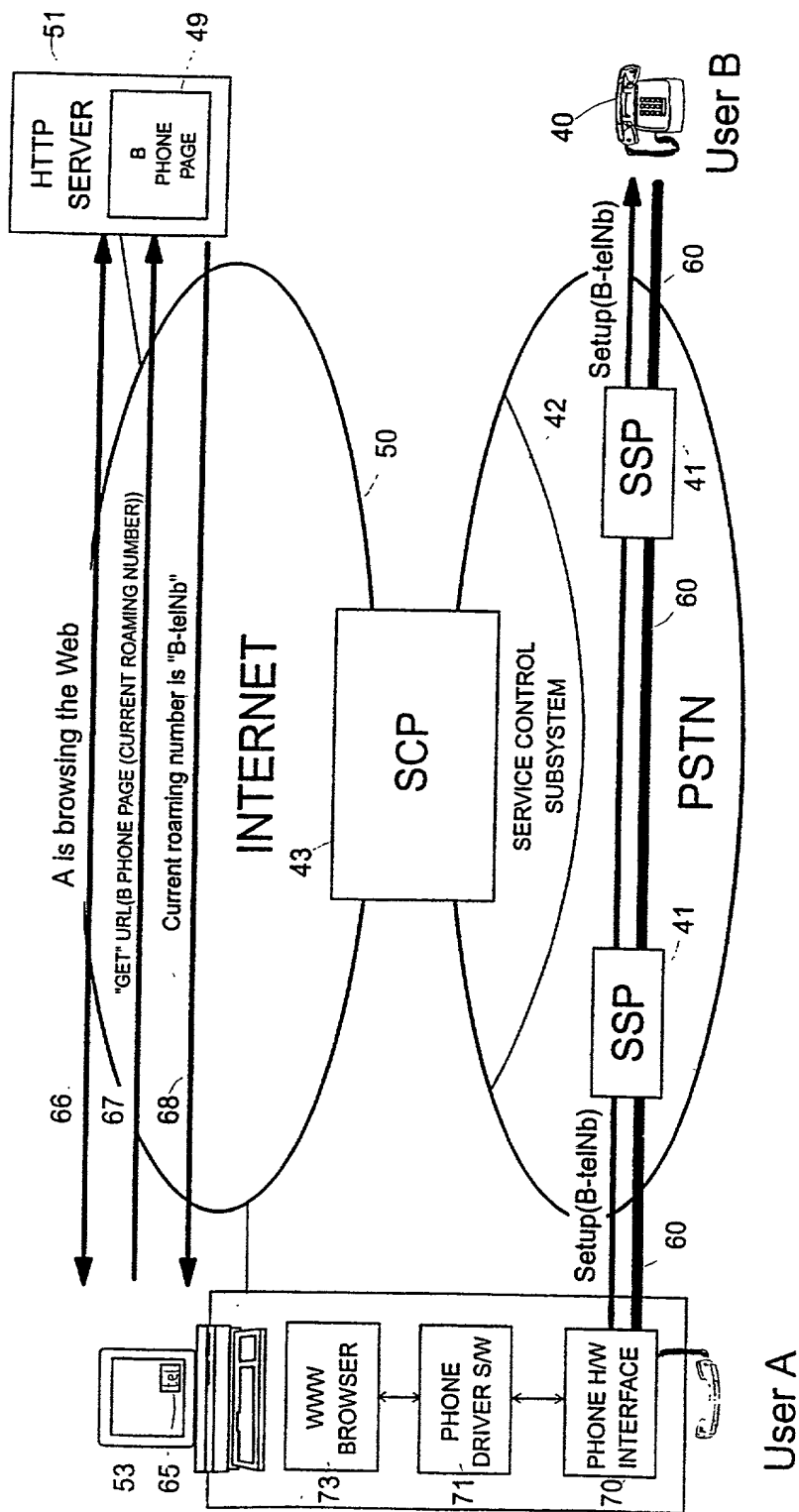


FIG. 14

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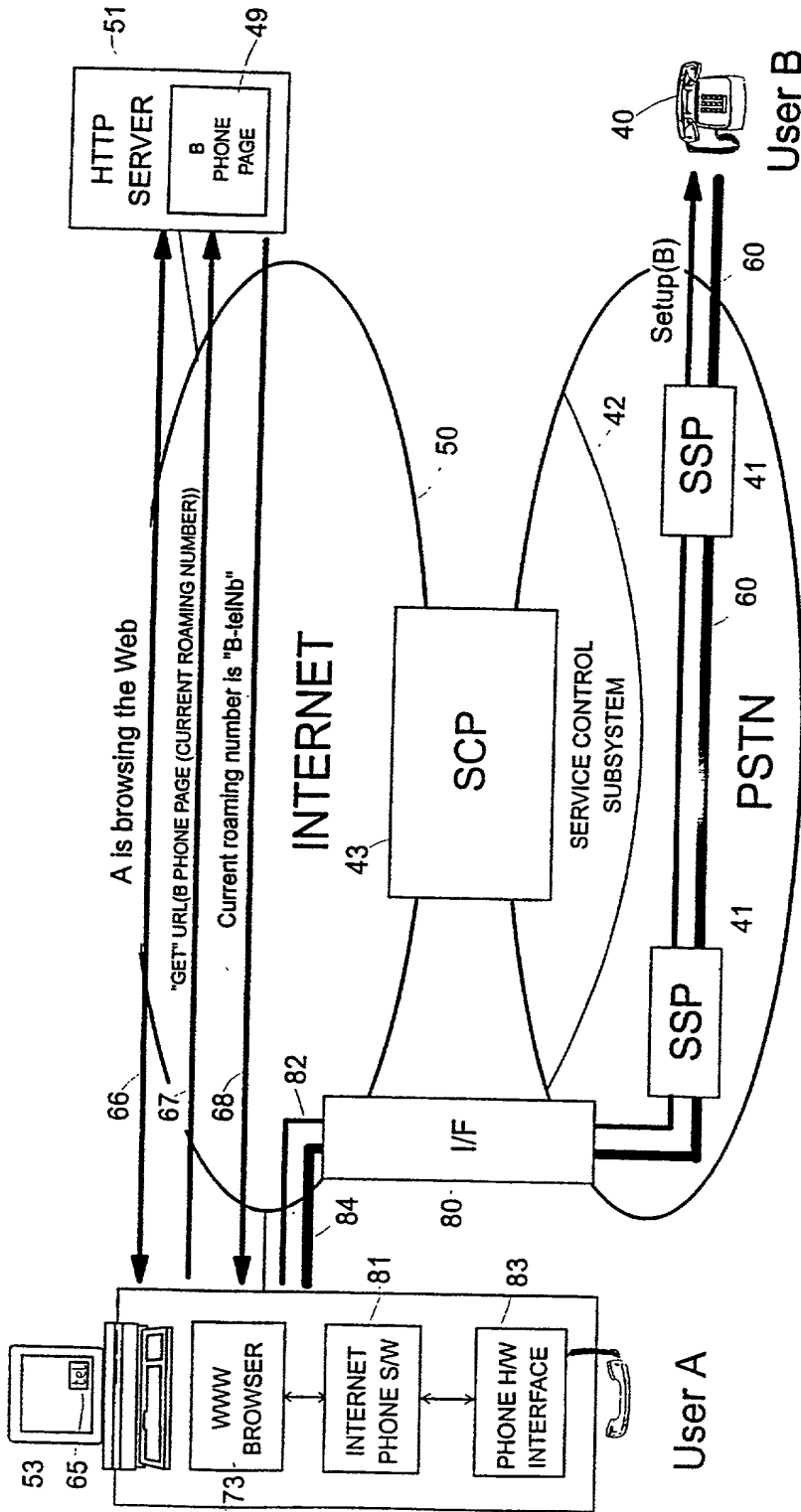


FIG. 15

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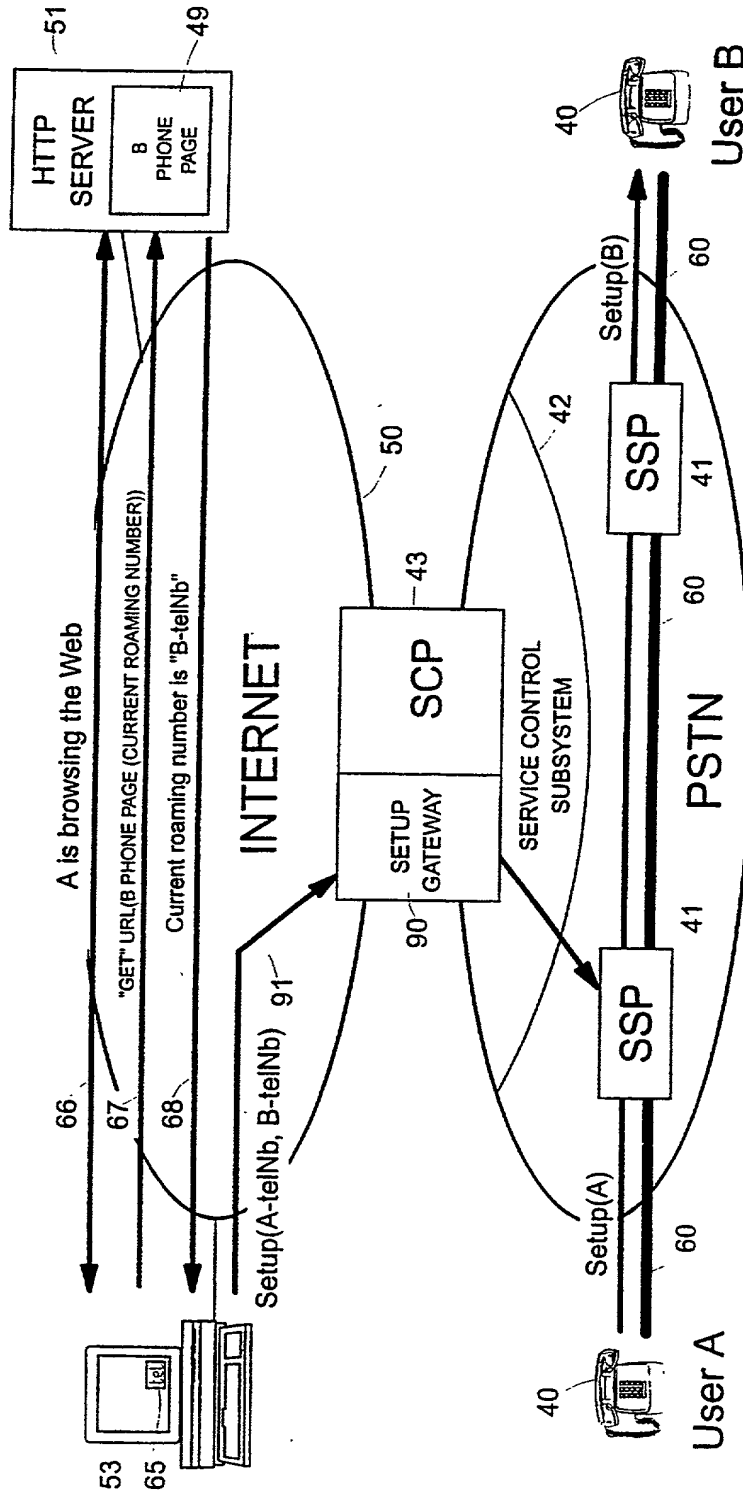


FIG. 16

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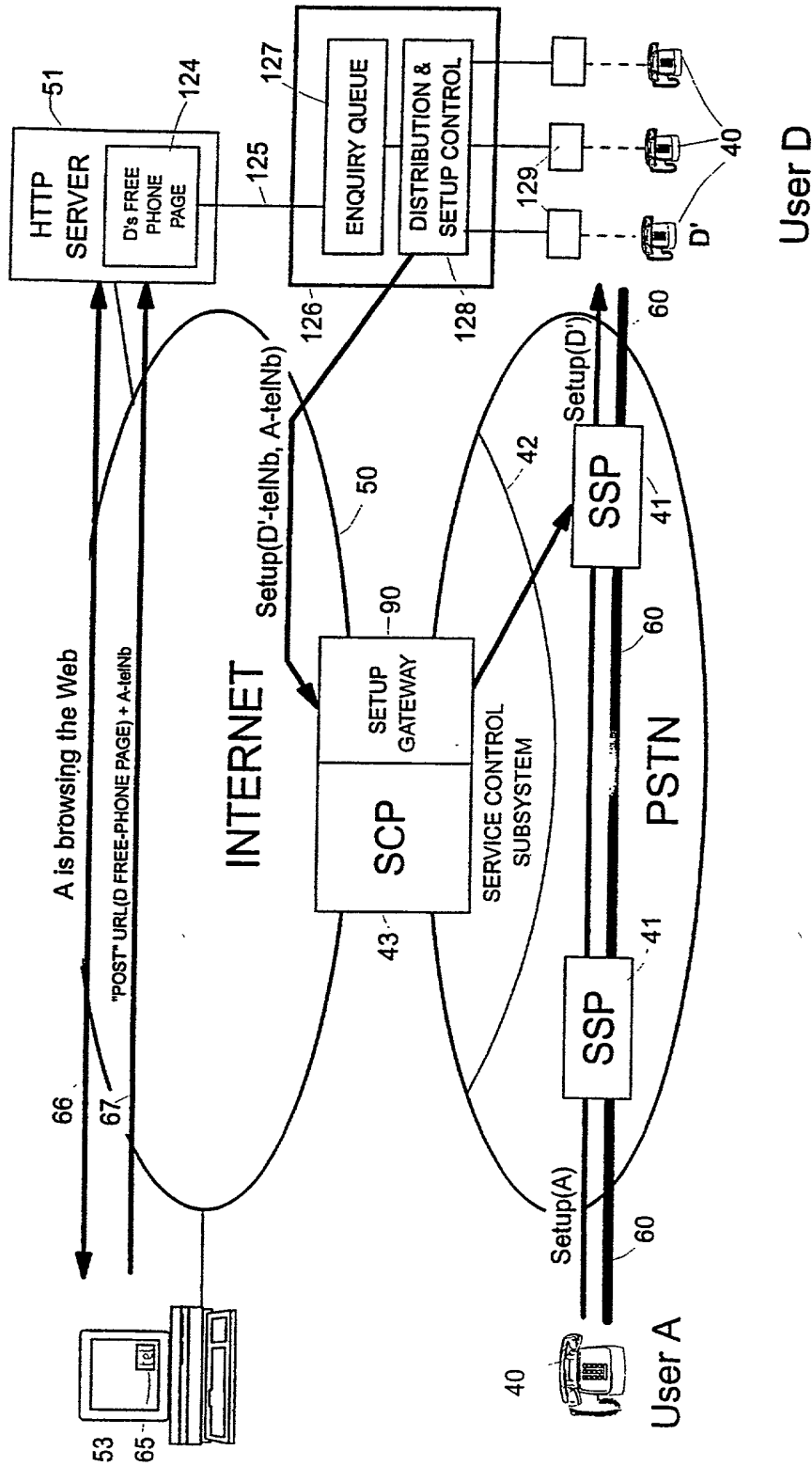


FIG. 17

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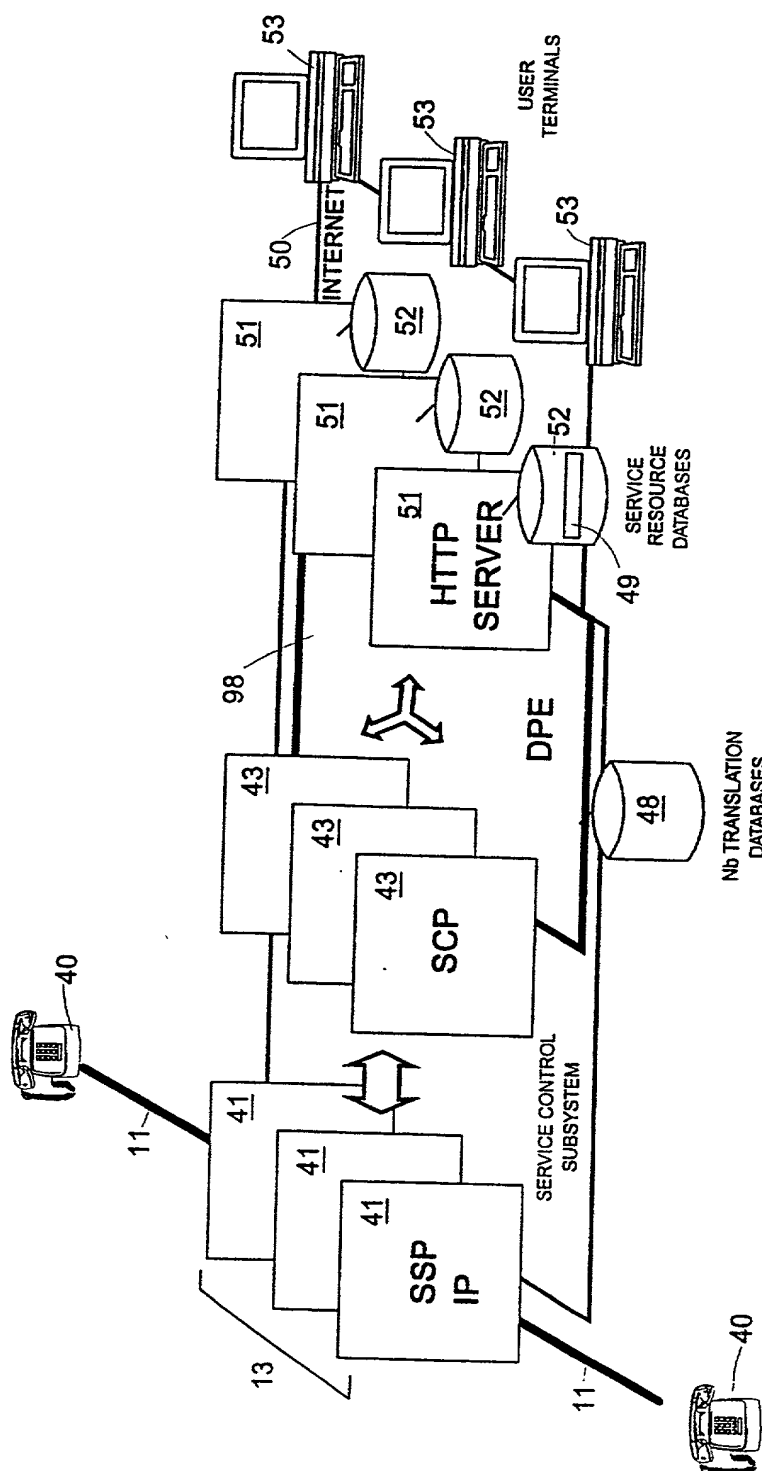


FIG. 18